

FIG 2

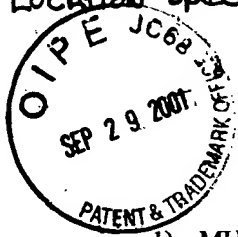


FIG: 1

DRAWINGS

- 1) MULTIPLE POSITION ENCODER
- 3) REAR VIEW OF UNIT HOUSING.
- 4) 8-BIT DIP SWITCH.
- 7) FRONT VIEW OF UNIT HOUSING.
- 10) INDEX LABELS.
- 11) FIXED ALARM OUTPUT PORT.
- 12) VOICE PLAYBACK OUTPUT PORT.

FIG: 2

BLOCK DIAGRAM

- 12) EXTERNAL INTERRUPT INPUT.
- 13) MICROCONTROLLER
- 16) ALARM IC.
- 17) VALID TRANSMISSION SIGNAL.
- 19) RECEIVER ENCODER.
- 22) FIRE SENSOR
- 25) ALARM DETECT OUTPUT.
- 26) REVERSE ANTENNA DEFAULT AND ENABLE MULTIPLE POSITION ENCODER.
- 27) ALARM DETECT OUTPUT.
- 28) PIEZO HORN.
- 31) ANTENNA SWITCH.
- 34) MULTIPLE POSITION ENCODER.
- 40) DECODER TRANSMITTER.
- 41) TRANSMIT ENABLE SIGNAL.
- 43) 4-BIT DATA BUS.
- 46) REVERSE DEFAULT STATE SIGNAL
- 49) 8-BIT DIP SWITCH.
- 50) INCOMING SIGNAL (8-BIT SYSTEM ID + 4-BIT INDEX).
- 51) DECODER TRANSMITTER OUTPUT.
- 52) OUTGOING SIGNAL (8-BIT SYSTEM ID + 4-BIT INDEX).
- 55) CUT HORN OUTPUT BEGIN VOICE-PLAYBACK SIGNAL.
- 58) VOICE CHIP.
- 60) INPUT TO RECEIVER ENCODER.
- 61) 4 TO 8-BIT CONVERTER.
- 63) FIRE SENSOR OUTPUT SIGNAL.
- 70) 4-BIT DATA OUTPUT.
- 74) 4-BIT INDEX OUTPUT.
- 80) 8-BIT SYSTEM ID.
- 84) 8-BIT ADDRESS CONVERTED FROM 4-BIT INDEX.